



Saved

- 31: (7) stubless adj2 configur\$6
- 36: (8) stubless near5 configur\$6
- 39: (461) point-to-point adj2 configur\$6
- 310: (43666) memory adj3 (module or board)
- 314: (44) 39 and 310
- 316: (297) stub adj2 configur\$6
- 317: (21) 310 and 316
- 321: (934837) terminat\$6
- 322: (1167) 310 same 321
- 323: (1169) 366/52
- 324: (3194) 366/63
- 326: (60301) "366"/\$6
- 326: (1664) 361/728
- 327: (706) 361/729
- 329: (7) 322 and 326
- 328: (4) 322 and 327
- 330: (62) 322 and 323
- 331: (87) 322 and 324
- 332: (342) 322 and 326
- 334: (28) (FUNABA near2 8EIJ).ln.
- 336: (236) (NISHIO near2 YOJI).ln.
- 336: (26) (SHIBATA near2 KAYOKO).ln.

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DB: US-PGPUB;USPAT;EPO;JPO;DERWENT;IBM_TOB

Default generator: OR

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BRS form ISIR form Image Text HTML

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Tagged (9)

	U		Document ID	Inventor	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Ct	S	C	P	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6661690 B2	Moriarty; Sharon L. et al.	20031209	19	High capacity memory module	366/63	366/52		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6142830 A	Loeffler; Steffen	20001107	7	Signaling improvement using	439/620	361/788		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6690781 B2	Kollipara; Ravindranath	20030708	28	Clock routing in multiple	361/760	174/250;		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6742098 B1	Halbert; John B. et al.	20040626	18	Dual-port buffer-to-memory	711/172	366/230.06;		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6772262 B1	Park; Myun-joo et al.	20040803	17	Memory module with improved	710/301	361/736;		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6438012 B1	Osaka; Hideki et al.	20020820	22	Directional coupling memory	366/52	366/230.03		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6392897 B1	Nakase; Yasunobu et al.	20020621	31	Circuit module	361/786	228/180.1;		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6619173 B2	Funaba; Seiji et al.	20030211	51	Memory system	366/63	366/99		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6172896 B1	Brown; Dirk D. et al.	20010109	14	High capacity memory module	366/63	366/52		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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